

REMARKS/ARGUMENTS

Figure 7 has been amended in order to correctly refer to step “705” consistent with detailed description of the present application.

Amendment to the specification, filed on 10/12/2005 was objected to due to informalities. As such, the Applicant has amended the specification to overcome this informality and withdrawal of this objection is earnestly solicited.

Claim rejections 35 USC § 102

Claims 1-28 were rejected under 35 USC 102(b) as being allegedly anticipated by Profit, Jr. U.S. Pat. No. 5,911,059 (hereinafter, Profit). The Applicant respectfully traverses the rejection.

Independent Claim 1 recites (emphasis added):

“A method for performing a sleep operation in a system that includes a device under test and an emulator device, said method comprising:

- a) executing instructions on said device under test;
- b) emulating the functions of said device under test by operating said emulator device in lock-step fashion with said device under test; and
- c) performing a sleep operation, comprising:
 - c1) upon receiving a first signal that indicates that a sleep function is to be performed, initiating said sleep function at said device under test;
 - c2) turning off one or more clock of said device under test; and
 - c3) discontinuing execution of instructions that are performed in lock-step by said emulator device upon turning off said clock.”

The present invention comprises an emulator device and a device under test.

The emulator device emulates the device under test by operating in lock-step fashion with the device under test.

Profit discloses a testing system that includes three components, a processor emulator, a communications interface and a host computer (see Profit, col. 5, lines 58-62 and Figure 7). Moreover, Profit discloses a system where part of the target hardware is modeled by the processor emulator and part of the target hardware is modeled by hardware simulator running on the host computer (see Profit, col. 5, lines 62-65). Therefore, the target hardware is modeled by the processor and the host computer. Therefore, Profit is directed to a simulator device. As such, Profit does not disclose a device under test and an emulator device operating in lock-step fashion with the device under test, as claimed. In contrast, the claimed invention recites a limitation for performing a sleep and stall operation where the emulator device operates in lock-step fashion with the device under test.

Accordingly, Profit neither teaches nor suggests the limitations of independent Claim 1. Other independent claims in the present invention are similar in scope to that of Claim 1. Therefore, independent Claims 1, 7, 10, 16, 21 and 25 are patentable over Profit under 35 U.S.C. 102(b). Dependent claims are patentable over Profit, under 35 U.S.C. 102(b) by virtue of their dependency. As such, allowance of Claims 1-28 is earnestly solicited.

For the above reasons, the Applicant requests reconsideration and withdrawal of rejections under 35 U.S.C. 102(b).

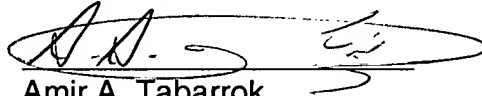
CONCLUSION

In light of the above listed remarks, reconsideration of the rejected Claims 1-28 is requested. Based on the arguments presented above, it is respectfully submitted that Claims 1-28 overcome the rejections of record and, therefore, allowance of Claims 1-28 is earnestly solicited.

Please charge any additional fees or apply any credits to our PTO deposit account number: 23-0085.

Dated: Feb 9th, 2006

Respectfully submitted,
WAGNER, MURABITO & HAO LLP



Amir A. Tabarrok
Registration No. 57,137

WAGNER, MURABITO & HAO LLP
Two North Market Street
Third Floor
San Jose, California 95113

(408) 938-9060 Voice
(408) 938-9069 Facsimile